ORYONE banknote validator

Quick guide

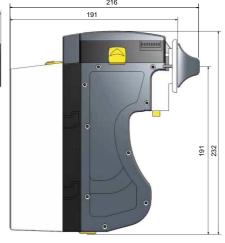
Rev. 1.03



All possible measures are taken by Alberici to maintain and improve the quality of this product. Unproper installation or incorrect use of this unit can mar its performances, as well as their stability in time.







Technical Specs:

ALIMENTAZIONE / POWER SUPPLY

24Vdc - ±5%

Assorbimento / Current draw 200 mA (stand-by) | 400 mA (work cycle, max 1 Amp)

PROTOCOLLI / INTERFACE

ccTalk / Pulse

TASSO DI ACCETTAZIONE / ACCEPTANCE RATE > 95%

TECNOLOGIE DI RICONOSCIMENTO / SCAN TECHNOLOGY Trasparenza e riflessione (sensori IR e sensori cromatici) VHR VHR transparency and reflection (IR and colour sensors)

VELOCITÀ DI VALIDAZIONE / VALIDATION SPEED

2 sec ca. (4 versi) / approx. 2 sec (any of 4 directions)

BANCONOTE COMPATIBILI / BANKNOTE SIZE

62 - 82,5 mm (larghezza/width)

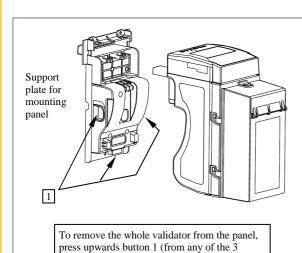
TEMPERATURA DI UTILIZZO / OPERATING TEMPERATURE

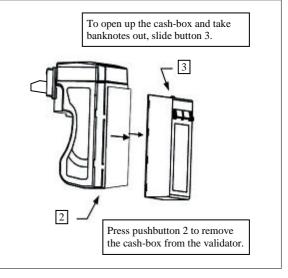
0°C ÷ 50°C (senza condensa/without condensation)

TEMPERATURA DI MAGAZZINO / STORAGE TEMPERATURE

-10°C ÷ 60°C (senza condensa/without condensation)

Peso / Weight 1,200 Kg





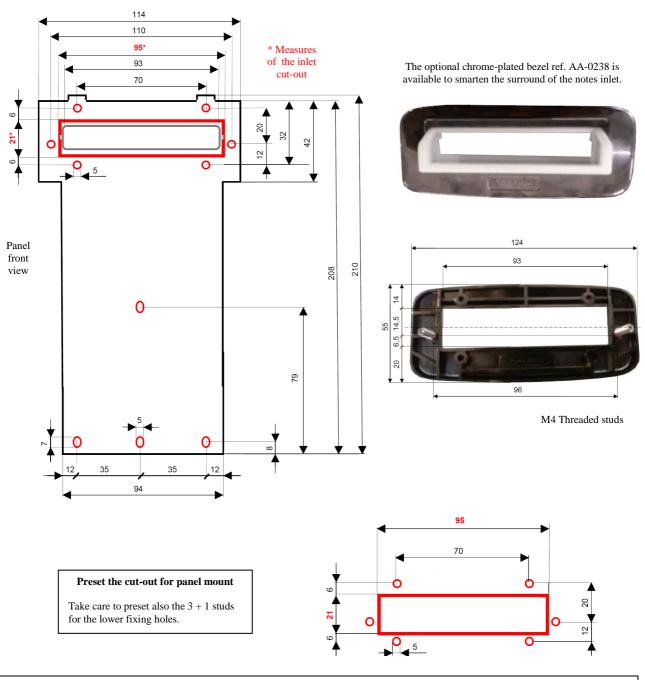


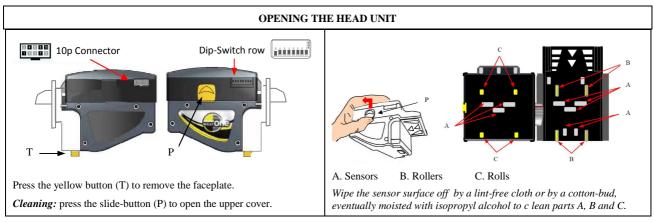
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positions shown in figure above)

FIXING PLATE for PANEL (MAX THICKNESS = 7 MM):





							CONNECTOR PIN-OUT			
CCTALK					Pin	Signal	Function	Pin	Signal	Function
DATA	1	2	2		1	CCT	CCT Data (active low)	6	NC	Not connected
-	3	4	GND	8 10 7 9	2	NC	Not connected	7	Vcc	+ 12 / 24 Vdc (Power supply)
=	5	6	2	വ	3	NC	Not connected	8	Vss	GND (Power supply)
12/24 Vdc	7	8	GND	4 m	4	NC	GND	9	NC	Not connected
	9	10	12/24 Vdc	1	5	NC	Not connected	10	Vec	+ 12 / 24 Vdc (Power supply)
PULSE					Pin	Signal	Function	Pin	Signal	Function
INH+	1	2	-		1	INH+	Inhibit (GND = enabled)	6	NC	Not connected
-	3	4	GND	8 10 7 9	2	NC	Not connected	7	Vcc	+ 12 / 24 Vdc (Power supply)
-	5	6	-	2 0	3	NC	Not connected	8	Vss	GND (Power supply)
12/24 Vdc	7	8	GND	4 m	4	NC	GND	9	VEND	Credit out (active low)
VEND	9	10	12/24 Vdc	7	5	NC	Not connected	10	Vcc	+ 12 / 24 Vdc (Power supply)

N° SW	DIP-SWITCH FUNCTIONS							
SW 1	OFF	High sele	ectivity – High security	ON	High a	acceptance – Low security		
SW 2	OFF		d, warning enabled: 5 attempts + temporary	y ON		Anti-fraud, warning disabled		
		inhibit (s	ee ** in Table AF Modes below)		(see *	in Table AF Modes below)		
SW 3 e SW 4	SW 3	SW 4		Set up of slot luminescence				
	OFF	OFF	Changing colours					
	ON	OFF	Green					
	OFF ON		Blue					
	ON	ON		White				
SW 5	Serial modes (Dip-Sw8=OFF)			Pulse Mode (Dip-Sw8=ON)				
	SW 5	Function		SW 5	Pulse wi	Pulse width		
	OFF	n.d.		OFF	100msec	00msec./100msec.		
	ON	n.d.		ON	e./200msec.			
		Seria	l modes (Dip-Sw8=OFF)	Pulse Mode (Dip-Sw8=ON)				
SW 6 e SW 7	SW 6	SW 7	Operating mode (protocol)	SW 6	SW 7	Pulse number		
	OFF	OFF	ccTalk	OFF	OFF	5 Euro = 1 pulse		
	ON	OFF	n.d.	ON	OFF	5 Euro = 5 pulses		
	OFF	ON	n.d.	OFF	ON	10 Euro = 5 pulses (5 € disabled)[v. 2.02 up]		
	ON	ON	n.d.	ON	ON	5 Euro = 10 pulses [v. 2.02 up]		
SW 8	OFF Serial mode (Select by Dip-switch 6 + 7: ex. ccTalk: SW6=OFF e SW7=OFF)							
SW 8	ON Pulse mode							

Please pay attention: after any change in the DS settings, power must be turned off and then on again, so that the validator can detect the set operation mode.

CCTALK COMMANDS						
CcTalk supported specifications list 1. cctalk Generic Specification Issue 3.2 2. cctalk Expansion for Bill Validators Issue2.1 CcTalk supported commands list 1. Core Commands Header 192 - Request build code Header 244 - Request product code Header 245 - Request equipment category id Header 246 - Request manufacturer id Header 254 - Simple poll	2. Core Plus Commands Header 001 - Reset device Header 004 - Request comms revision Header 241 - Request software revision Header 242 - Request serial number 3. Bill Validator Commands Header 145 - Request currency revision Header 152 - Request bill operating mode Header 153 - Modify bill operating mode Header 154 - Route bill	Header 156 - Request country scaling factor Header 157 - Request bill id Header 159 - Read buffered bill events Header 197 - Calculate ROM checksum Header 213 - Request Option flags Header 216 - Request data storage availability Header 227 - Request inhibit status Header 228 - Modify master inhibit status Header 230 - Request inhibit status Header 247 - Request variable set				

LUMINESCENT SIGNALS

NR RED FLASHES	DESCRIPTION			
1	VALIDATOR IS OPEN			
2	JAMMED BANKNOTE			
3	FRAUD ATTEMPTED			
5	ADJUST OPTICS			
7	-			
9	LOW POWER SUPPLY			
11	CHECK ENCODER+MOTOR EFFICIENCY			
12	-			
14	ROM ERROR			

AF Modes (Anti-fraud)

(*) Dip-Switch SW2 ON					
After fraud attempts, the validator returns the banknote. No signal given.					
(**) Dip-Switch SW2 OFF					
Attempt	Validator reaction	Do as described below			
1°	Remains in service	-			
2°	Remains in service	-			
3°	> error (3 red flashes)	Reset (switch off then on)			

... 4° ... > error (3 red flashes) Reset (switch off then on)

After the 5th fraud attempt (3 yellow flashes), it is necessary to wait for automatic restore of service. Take care not to switch the device off.

